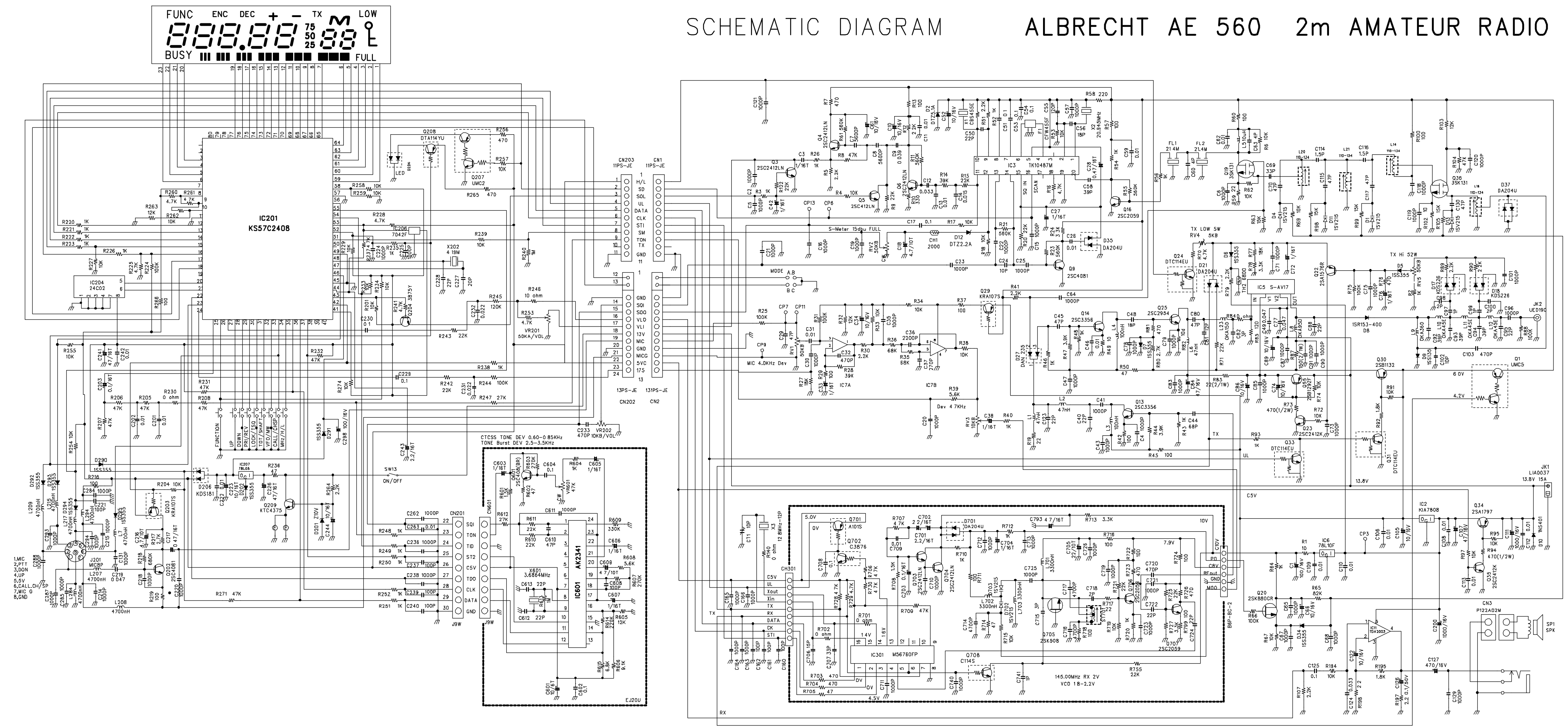
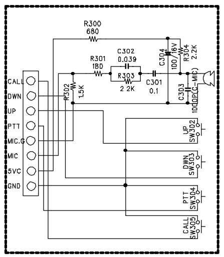
SCHEMATIC DIAGRAM ALBRECHT AE 560 2m AMATEUR RADIO





AK2341 CTCSS Encoder Decoder

Port No.	I/O	Logic	Pin Name	Function
1	I		RXIN	RX Signal Input
2	D		RXION	AMP2 Output
3	D		TXION	AMP1 Output
4	I		TXIN	TX Audio Input
5	D		RXOUT	RX Audio Output
6	D		TXOUT	TX Audio Output
7			VDD	Power Suppiy(1.8 – 5.5V)
8	I		XIN	Crystal Terminal (3.6864MHz)
9	D		XOUT	Crystal Terminal (3.6864MHz)
10	I		STB	Strobe for Serial Data
11	I		SDATA	Serial Data
12	I		SCLK	Serial Clock
13	I		DCS	DCS Input
14	D		DETOUT	Tone Detection Output(Detect: Low)
15			VSS	Ground
16	I		DREF	Tone Detection Level Adjust Input
17	I		TLINP	RX Tone Signal Reference Input
18	I		TLINN	RX Tone Signal Input
19	D		TLINO	AMP3 Output
20	D		RXTONE	RX Tone Signal Input
21	D		TXTONE	TX Tone Signal Output
22	I		AGNDIN	Analog Ground Input
23	D		AGND	Analog Ground Output
24	I		BIAS	Bias Input

KS57C2408 Terminal Function of Microprocessor

Port No.	I/O	Logic	Pin Name	Description
01	O		SEG22	NO USE
02	O		SEG23	NO USE
03	O		COM0	LCD SEGMENT 20
04	O		COM1	LCD SEGMENT 21
05	O		COM2	LCD SEGMENT 22
06	O		COM3	LCD SEGMENT 23
07	I		BIAS	LCD POWER CONTROL
08	I		VLC0	LCD VOLTAGE DIVIDING
09	I		VLC1	LCD VOLTAGE DIVIDING
10	I		VLC2	LCD VOLTAGE DIVIDING
11	I		VSS	GND
12		CLOCK	SCK	CLOCK (PLL IC)
13		CLOCK	SO	DATA (PLL IC)
14		CLOCK	SI	ST1 (PLL IC)
15	I	Active Low	BTC0	UNLOCK
16	I	Active High	INT0	SIGNAL DETECTOR
17	I		INT1	BAND PLAN
18	I		INT2	BAND PLAN
19	I	Active Low	INT4	LOW BATTERY
20		CLOCK	TCL0	DO (EEPROM)
21		CLOCK	TCL1	SCLOCK (EEPROM)
22	I	Active High	P2.2	TDO (CTCSS) OPTION INPUT DETECTION
23			P2.3	
24	O		TCLO0	1.75K OUTPUT
25	O		TCLO1	88.5Hz TONE OUTPUT
26	I	Active Low	CL0	FUNCTION KEY INPUT
27	O		BUZ	BEEP (OUT)
28	I	Active Low	KS0	UP (KEY INPUT)
29	I	Active Low	KS1	DN (KEY INPUT)
30	I	Active Low	KS2	PRVREV (KEY INPUT)
31	O	Active Low	KS3	LOCK/T.SQ (KEY INPUT)
32	I	Active Low	KS4	TOT/SHAFT (KEY INPUT)
33	I	Active Low	KS5	VFO/MW (KEY INPUT)
34	I	Active Low	KS6	CALL/CHSP (KEY INPUT)
35	I	Active Low	KS7	MHz/HL (KEY INPUT)
36		CLOCK	P5,0	CLOCK (CTCSS)
37		CLOCK	P5,1	DATA (CTCSS)
38		CLOCK	P5.2	ST2 (CTCSS)
39			P5,3	NO USE
40			P4,0	NO USE
41	O	Active High	P4,1	HI–LO OUTPUT
42	O	Active Low	P4,2	MIC/MUTE OUTPUT
43	O	Active High	P4,3	SOL (OUTPUT)
44	I	Active Low	RESET	RESET (INPUT)
45	I		TEST	GND
46	I		VDD	POWER SUPPLY INPUT 5V(MAIN)
47	O		X OUT	CRYSTAL O.S.C OUT
48	I		X IN	CRYSTAL O.S.C OUTPUT
49	I		XT IN	NO USE (EXT O.S.C IN)
50	O		XT OUT	NO USE (EXT O.S.C OUT)
51	I	ANALOG INPUT	P9,0(AD0)	SIGNAL METER INPUT
52	I	Active Low	P9,1(AD1)	TX INPUT
53	I	Active Low	P9,2(AD2)	BACK UP SINGAL INPUT
54	I		P9,3(AD3)	TID (CTCSS) TONE UNIT DETECTION INPUT
55	I	Active Low	P10,0(AD4)	MIC(UP) INPUT(KEY)
56	I	Active Low	P10,1(AD5)	MIC(DN) INPUT(KEY)
57	I		AVSS	GROUND (MAIN)
58	I		AVREF	SIGNAL INPUT REFERENCE VOLTAGE
59	O		SEG 0	LCD SIGMENT 1 OUTPUT
60	O		SEG 1	LCD SIGMENT 2 OUTPUT
61	O		SEG 2	LCD SIGMENT 3 OUTPUT
62	O		SEG 3	LCD SIGMENT 4 OUTPUT
63	O		SEG 4	LCD SIGMENT 5 OUTPUT
64	O		SEG 5	LCD SIGMENT 6 OUTPUT
65	O		SEG 6	LCD SIGMENT 7 OUTPUT
66	O		SEG 7	LCD SIGMENT 8 OUTPUT
67	O		SEG 8	LCD SIGMENT 9 OUTPUT
68	O		SEG 9	LCD SIGMENT 10 OUTPUT
69	O		SEG10	LCD SIGMENT 11 OUTPUT
70	O		SEG11	LCD SIGMENT 12 OUTPUT
71	O		SEG12	LCD SIGMENT 13 OUTPUT
72	O		SEG13	LCD SIGMENT 14 OUTPUT
73	O		SEG14	LCD SIGMENT 15 OUTPUT
74	O		SEG15	LCD SIGMENT 16 OUTPUT
75	O		SEG16	LCD SIGMENT 17 OUTPUT
76	O		SEG17	LCD SIGMENT 18 OUTPUT
77	O		SEG18	LCD SIGMENT 19 OUTPUT
78	O		SEG19	NO USE
79	O		SEG20	NO USE
80	O		SEG21	NO USE